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PERSONAL DATA

Name: Shuja Ahmad Abbasi

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King Saud University
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EDUCATION

Aligarh Muslim University, Aligarh, India

Year	Degree	Faculty	Field
1970	B.Sc.	Engineering	Electrical Engineering
1972	M.Sc.	Engineering	Electrical Engineering

University of Southampton, Southampton, England

Year	Degree	Faculty	Field
1980	Ph.D.	Engineering	Electronics Engineering

CURRENT POSITION

Professor, Electrical Engineering Department, King Saud University, Riyadh, Saudi Arabia

POSITIONS HELD

ACADEMIC - KING SAUD UNIVERSITY, RIYADH, SUDI ARABIA

Electrical Engineering Department

1. Professor (1999-present)

ACADEMIC - ALIGARH MUSLIM UNIVERSITY, ALIGARH, INDIA

Department of Electrical/Electronics Engineering

1. Professor (1986-1999)
2. Associate Professor (Reader) (1982-1986)

PROFESSIONAL MEMBERSHIPS

- Senior Member IEEE, USA
- Fellow Institution of Electronics and Telecommunication Engineers, India.

AWARDS

GOVERNMENT AWARDS

Gold medal for standing first in all branches of Engg. in B.Sc. Engg. 1970.

TECHNOLOGY AWARDS

2. xx

AWARD PUBLICATIONS

1. **Best Paper Award** for the paper presented at National Conf. on Signals, System Modelling, Simulation and Analysis (SDS-95), Bhatkal, Karnataka, India, Dec. 1995.
2. **Best Paper Presentation Award** for the paper presented at the National Systems Conference (NSC 96), Thiruvanthapuram, India, Dec.1996.

PUBLICATIONS (*Summary*)

- Refereed Journal Papers (14)
- Invited / Magazine Papers (01)
- Award Winning Conference Papers (02)
- Conference Papers (49)
- Book Contributions (-)
- Utility Industry Technical Reports (-)
- University Technical Reports (-)
- National and International (Externally-Funded) Project Official Final Reports (08)
- Final Industry Consulting Reports (Industry Funded Projects) (-)
- University (Research Funded) Project Final Reports (03)

ACADEMIC SUPERVISION

GRADUATE/UNDERGRADUATE SUPERVISION

Ph.D. Programs: 02 students
M.Eng. & M.Sc. Programs: 16 students
Graduation Thesis & Summer Projects: 150 students

GRADUATE/UNDERGRADUATE EXAMINATION & SUPERVISORY COMMITTEES

Ph.D. Supervisory Committees: 02 students
Ph.D. & M.Eng. Defense Committees: 20 students
B.Sc. Thesis Examination Committees: 60 students

RECENT R&D FUNDED CONTRACTS

(*Last Five Years*)

1. Research Center, College of Engg., King Saud University, Riyadh, Project No.: 421-50
   2001-2003: Design and Prototyping of Digital Processors using Reconfigurable Logic (SR 30,000)
2. Deanship of Scientific Research, King Saud University, Riyadh, Project No.: DSR-AR-53
   2003-2004: A State – of – the – Art VLSI Design Center (SR 140,000)
3. Research Center, College of Engg., King Saud University, Riyadh, Project No.: 425-05
   2004-2005: FPGA Based Design and Implementation of Precision Multi-Function Generators (SR 50,000)
4. Boeing Middle East, Riyadh
   2004-2005: A State - of - the - Art ASIC Design Center (SR 247,800)
5. Research Center, College of Engg., King Saud University, Riyadh, Project No.: 426-07
   2005-2006: Hardware Realization of Algorithms for Computation of Cumulants (SR 48,200)

RECENT PUBLICATIONS (*Last Five Years*)

Journal Papers

1. Abha Govil and Shuja A. Abbasi, 'Testable Design of Binary Systems Using Spectral Data', Jl. of Engineering, Vol. 12, no. 2, pp 89 – 105,2002.
2. S.A.Abbasi, Ateeq A. Khan and F. Rahman, 'Investigation and Modeling of Masking Oxide Effects on Boron Diffusion into Silicon', Jl. of Engineering, Vol. 12, no. 2, pp 107 – 117, 2002.
3. Mohammad Hasan, V. Mohan and Shuja A. Abbasi, 'A Novel User Friendly PC Based IC Layout Editor and Design Rule Checker', Jl. of Engineering, Vol. 14, no. 1, pp 7–12, 2004.
4. Hasan M, Arora P, Singhal M and Abbasi SA, 'VHDL modeling and FPGA based implementation of a memory efficient Huffman decoder', Jl. IETE Technical review, vol. 21, No. 6, pp-371-377, 2004.

5. Ateeq A. Khan, Tehzeeb A. Abbasi and Shuja A. Abbasi, 'A Simulator for Window Frame Width Effect on Boron Diffusion through Silicon', Jl. of Engineering, Vol. 15, no. 1, pp 29–38, 2005.

Conference Papers

6. Amin M. A. Bin Ateeq, Shuja A. Abbasi and A. R. M. Alamoud, 'Hardware Realization of Walsh Functions and Their Applications Using VHDL and Reconfigurable Logic', ICM – 2002, Beirut, Lebanon, pp 58 – 61, 2002.
7. Shuja A. Abbasi and Abha Govil, 'Design of Digital Circuits/Systems with Built-in Testability', ICM – 2002, Beirut, Lebanon, pp 228 – 231, 2002.
8. Shuja A. Abbasi and Ateeq A. Khan, 'Three Dimensional Modeling of Anomalous Diffusion of Boron through Patterned Silicon', ISSM2003, San Jose, California, USA, pp415 – 418, 2003.
9. Syed Manzoor Qasim and Shuja A. Abbasi, 'FPGA Implementation of a Single-Channel HDLC Layer-2 Protocol Transmitter using VHDL', ICM – 2003, Cairo, Egypt, pp265–268, 2003.
10. S. A. Abbasi, 'FPGA Based Realization of a Reduced Complexity High Speed decoder for error Correction', 10th IEEE International Conf. on Electronics, Circuits and Systems (ICECS-2003), Sharjah, UAE, pp 1002 – 1005, 2003
11. Syed Manzoor Qasim and Shuja A. Abbasi, 'Hardware Realization of a Single-Channel HDLC Protocol Transmitter using FPGA', Proceedings of GSPX(The Embedded Signal Processing Conference), Santa Clara, California, September 27 - 30, 2004.
12. Syed Manzoor Qasim and Shuja A. Abbasi, 'FPGA in Cryptographic Applications: A Survey', Proceedings of MTECS, Aligarh, India, pp 87-90, 2005.