

DATE

Sept./21/2008

PERSONAL DATA

Name: Mohammed A ALTURAIGI

Business Address:

King Saud University
Electrical Engineering Department,
P.O. Box 800, Riyadh 11421, Saudi Arabia
Telephone (966-14676797
E-mail: mturaigi@ksu.edu.sa

Home Address: St.7 Villa # 6 University Housing

EDUCATION

King Saud University

<u>Year</u>	<u>Degree</u>	<u>Faculty</u>	<u>Field</u>
1976	B.Sc.	Engineering	Electrical Engineering

Syracuse University

1979	M.Sc.	Engineering	Electric Engineering
------	-------	-------------	----------------------

Syracuse University

<u>Year</u>	<u>Degree</u>	<u>Faculty</u>	<u>Field</u>
1983	Ph.D.	Engineering	Electrical Engineering

CURRENT POSITION

Professor, Electrical Engineering Department, King Saud University, Riyadh, Saudi Arabia

POSITIONS HELD

ACADEMIC - KING SAUD UNIVERSITY, RIYADH, SUDI ARABIA

Electrical Engineering Department

Professor 3/16/1997-present)

ACADEMIC – KING SAUD UNIVESSITY

Electrical Engineerinf Department

1. Associate Professor (12/4/1988-3/16/1997)
 2. Assistant Professor (11/1983-12/4/1988)
-

PROFESSIONAL MEMBERSHIPS

- IEEE
 - ACM
-

AWARDS

GOVERNMENT AWARDS

Xxx

TECHNOLOGY AWARDS

2. xx

AWARD PUBLICATIONS

1. xxx

PUBLICATIONS (*Summary*)

- Refereed Journal Papers (21)
 - Conference Papers (20)
-

ACADEMIC SUPERVISION

GRADUATE/UNDERGRADUATE SUPERVISION

- Ph.D. Programs: ## students
- M.Eng. & M.Sc. Programs: ## students
- Graduation Thesis & Summer Projects: ## students

GRADUATE/UNDERGRADUATE EXAMINATION & SUPERVISORY COMMITTEES

- Ph.D. & M.Eng. Defense Committees: 20 students
 - B.Sc. Thesis Examination Committees: 50 students
-

RECENT R&D FUNDED CONTRACTS

(*Last Five Years*)

1. Xxx
xxx-xxx: Xxx (SR xxx,00)
 2. Xxx
xxx-xxx: Xxx (SR xxx,00)
 3. Xxx
xxx-xxx: Xxx (SR xxx,00)
-

RECENT PUBLICATIONS (*Last Five Years*)

Journal Papers

1. "A concurrent VLSI system for the computation of Higher Order Moments" Signals, Circuits and systems, 1999
2. " A new approach for computing Higher-Order Moments on Linear Systolic Arrays" Canadian Journal of Electrical Engineering.
3. "A VLSI system to speedup the computation of Ghost Canceller Parameters" IEEE Trans. Of Consumer Electronics, Aug. 1996

Conference Papers

4. "Cross Bi-cumulants Computation Using VLSI Arch." CAINE-97, San-Antonio, USA.
5. Xxx
6. Xxx